IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Wills et al.

(TI-37082)

Conf. No. 9565

Serial No. 10/749,416

Group Art Unit: 2863

Filed: December 31, 2003

Examiner: Bui

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For: Wavelet Analysis of One or More Time Domain Reflectometry (TDR) Signals to

Determine One or More Characteristics of One or More Anomalies in a Wire

DECLARATION OF KENDALL SCOTT WILLS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

I, Kendall Scott Wills, hereby declare:

- 1. I am one of the named inventors in this patent application.
- 2. I have been employed by Texas Instruments Incorporated, in Dallas, Texas, since at least as early as May 12, 2003.
- 3. On information and belief, Exhibit A to this Declaration is a copy of pages of an engineering notebook prepared by Michael Dockins, also one of the named inventors in this patent application, during his employment at Texas Instruments Incorporated. This engineering notebook describes a project that Michael Dockins worked on with me during his employment at Texas Instruments Incorporated, in the United States, in the summer of 2002. This work was performed by us at least as early as May 12, 2003.

- 4. Pages 14 and 15 of Exhibit A describe the concept of time domain reflectometry (TDR) and the capability of TDR to locate circuit features physically. Page 15 also describes our idea that "Comparative TDR", which uses multiple TDR waveforms of objects with known circuit features, and which compares TDR waveforms of unknown circuit features, with the similarities and differences providing information about the circuitry associated with the unknown waveforms. These pages of the engineering notebook were prepared by Michael Dockins in the United States at least as early as May 12, 2003.
- 5. Pages 40 through 45 of Exhibit A describe our conception of the idea of using a wavelet transform (WT) in TDR. As described on page 40, the wavelet transform allows both time and frequency resolution to be changed based on the frequencies being examined; this property can be used to overcome limitations of conventional TDR. These pages of the engineering notebook were prepared by Michael Dockins in the United States at least as early as May 12, 2003.
- 6. Pages 54 through 59 of Exhibit A describe our idea that anomalies of packaged integrated circuit devices can be identified by calculating wavelet power spectra using wavelet transforms, and by then comparing the wavelet analysis results among different integrated circuit devices to determine similarities. These pages of the engineering notebook were prepared by Michael Dockins in the United States at least as early as May 12, 2003.
- 7. Pages 61 through 65 of Exhibit A describe a successful experiment in analyzing actual integrated circuit devices according to this technique. Pages 68 and 69 of Exhibit A describe the system and software used to perform this experiment. Page 64 illustrates instantaneous power spectra of wavelet transforms of signals applied to a integrated circuit device under test and to reference devices. In each case, these instantaneous power spectra are shown as a function of time from the launch of a time domain reflectivity (TDR) signal, with the density of each plotted point representing the power at the corresponding time and frequency values. As described in those pages, the integrated circuit device under test showed a failure in the connection between a solder bump connection and its die prior to stress ("pre-stress"). The measured instantaneous power spectrum of the wavelet transform for the device under test, pre-

stress (in its failed condition), which is shown in the upper left of page 64, resembled the instantaneous power spectrum shown in the upper right of page 64 for a reference package with no die (which would provide a similar electrical characteristic to a bump-to-die connection failure). After stressing of the device under test and the resulting recovery of that connection failure, the instantaneous power spectrum of the wavelet transform of the device under test, shown in the lower left of page 64, resembled that of a known good device, shown in the lower right of page 64. This experiment was performed in the United States at least as early as May 12, 2003, and these pages of the engineering notebook were prepared by Michael Dockins in the United States at least as early as May 12, 2003.

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- 8. Pages 72 and 73 of Exhibit A describe conclusions from our experiment, in that the wavelet transform technique allows time-frequency analysis of TDR signals, which is useful in identifying circuit and defect properties. These pages of the engineering notebook were prepared by Michael Dockins in the United States at least as early as May 12, 2003.
- 9. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Kendall Scott Wills

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Date: 6/22

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EXHIBIT A

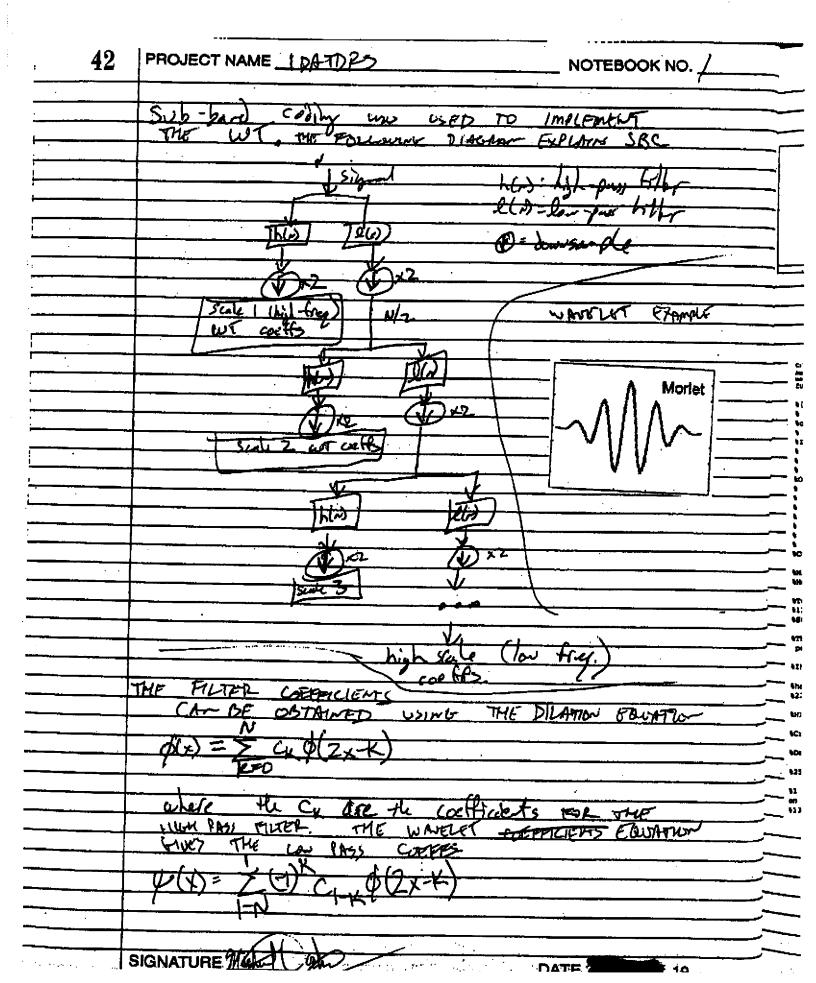
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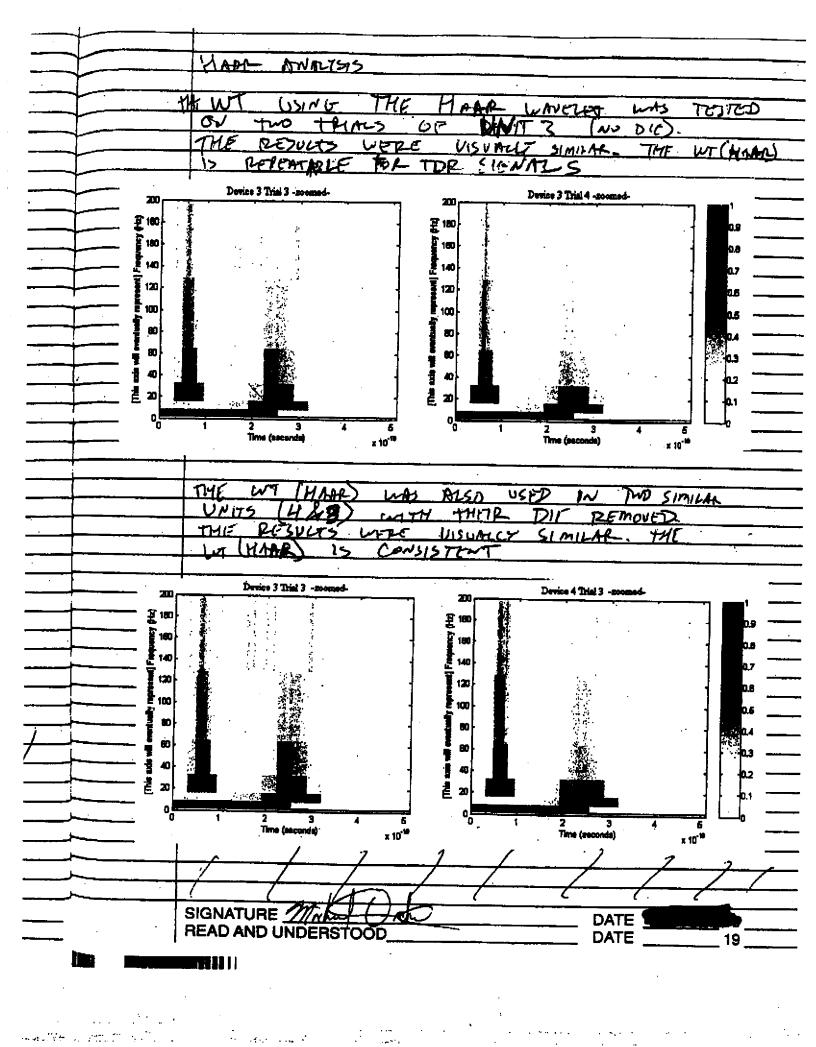


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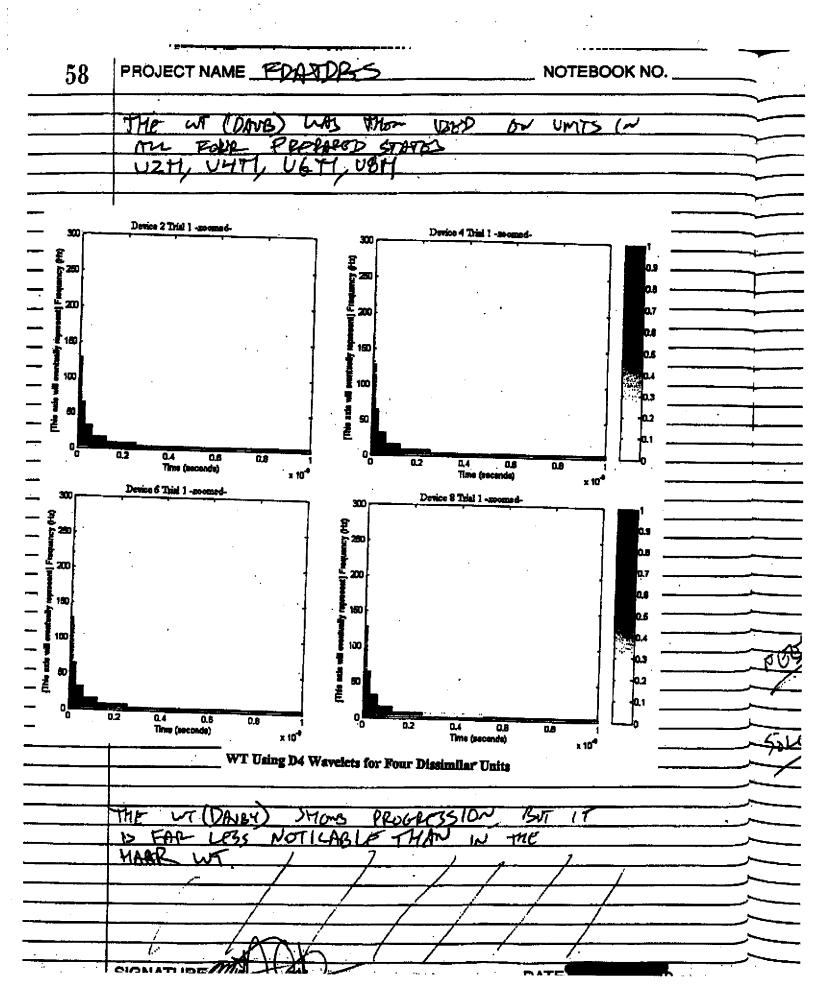
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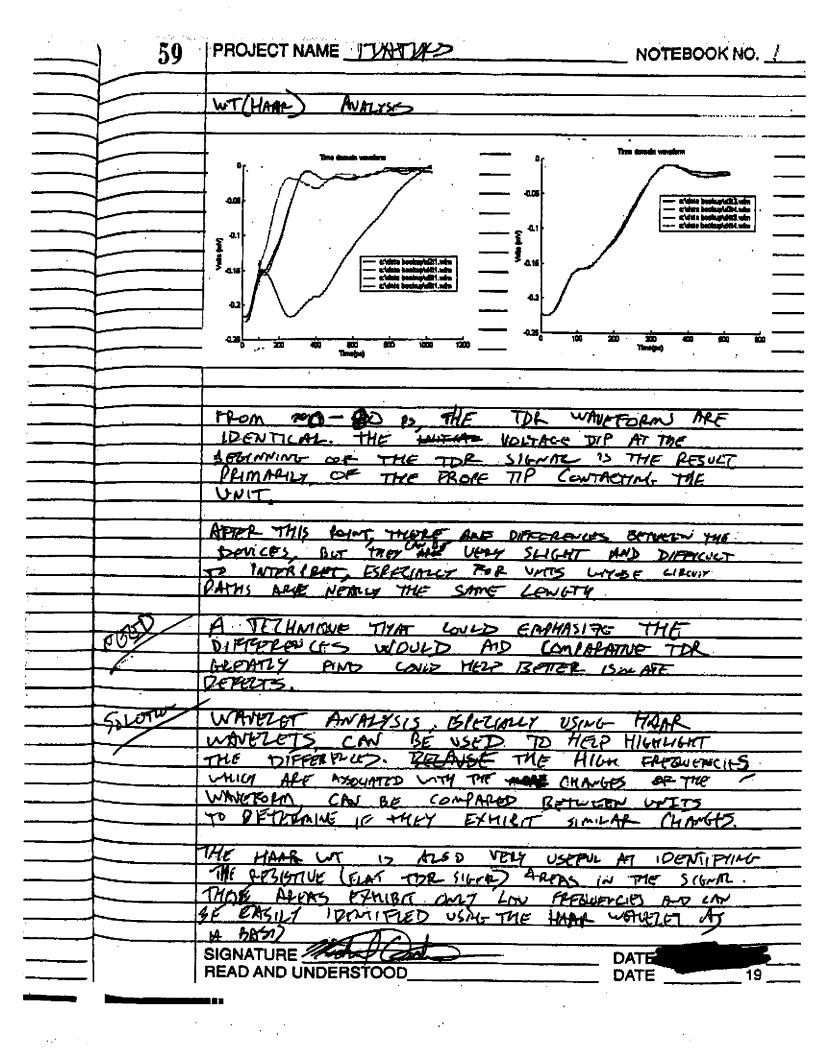
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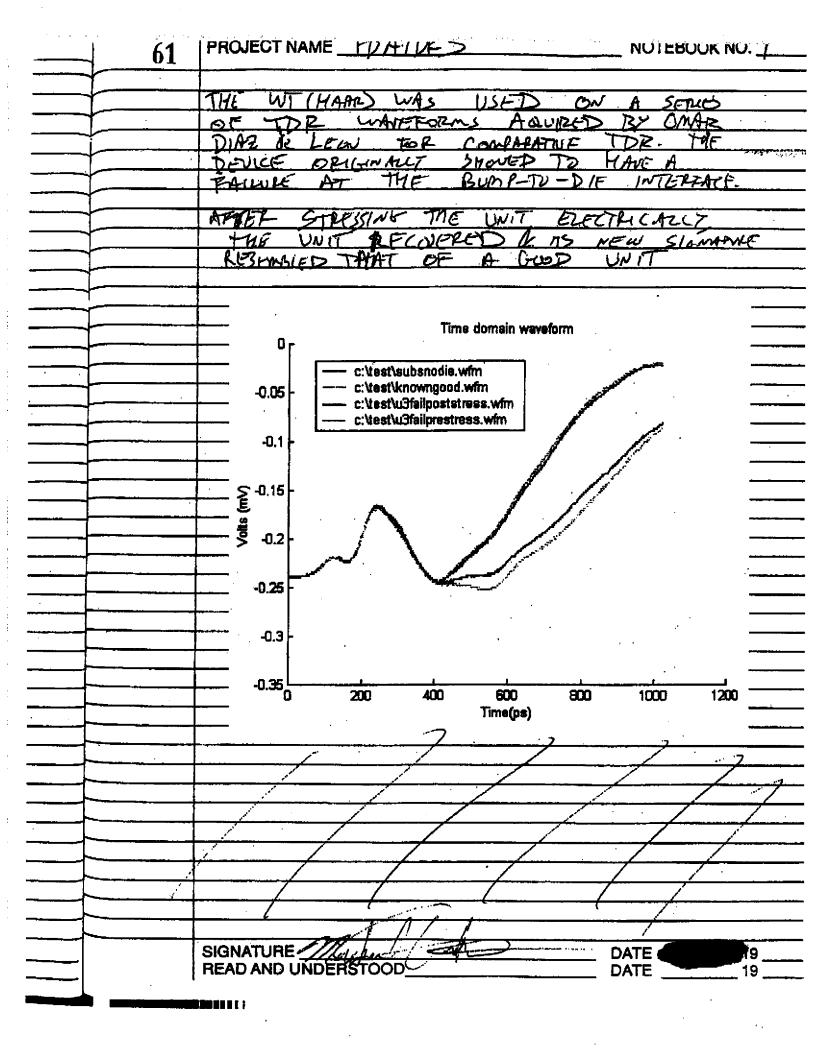
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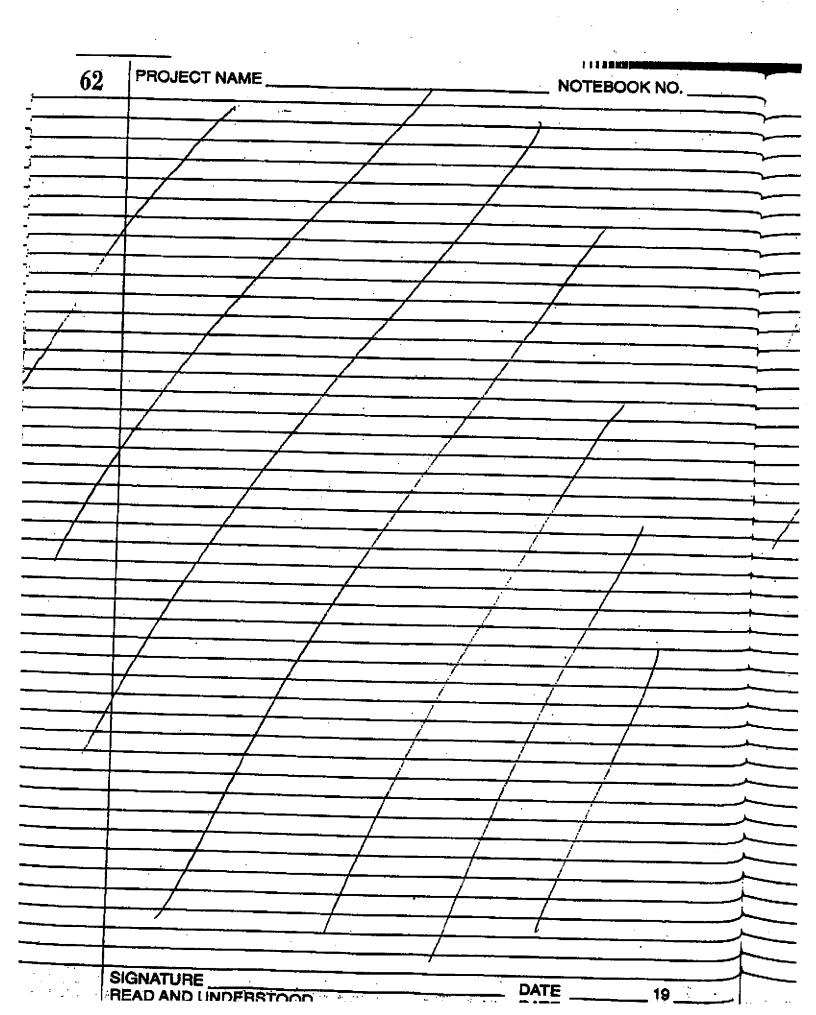


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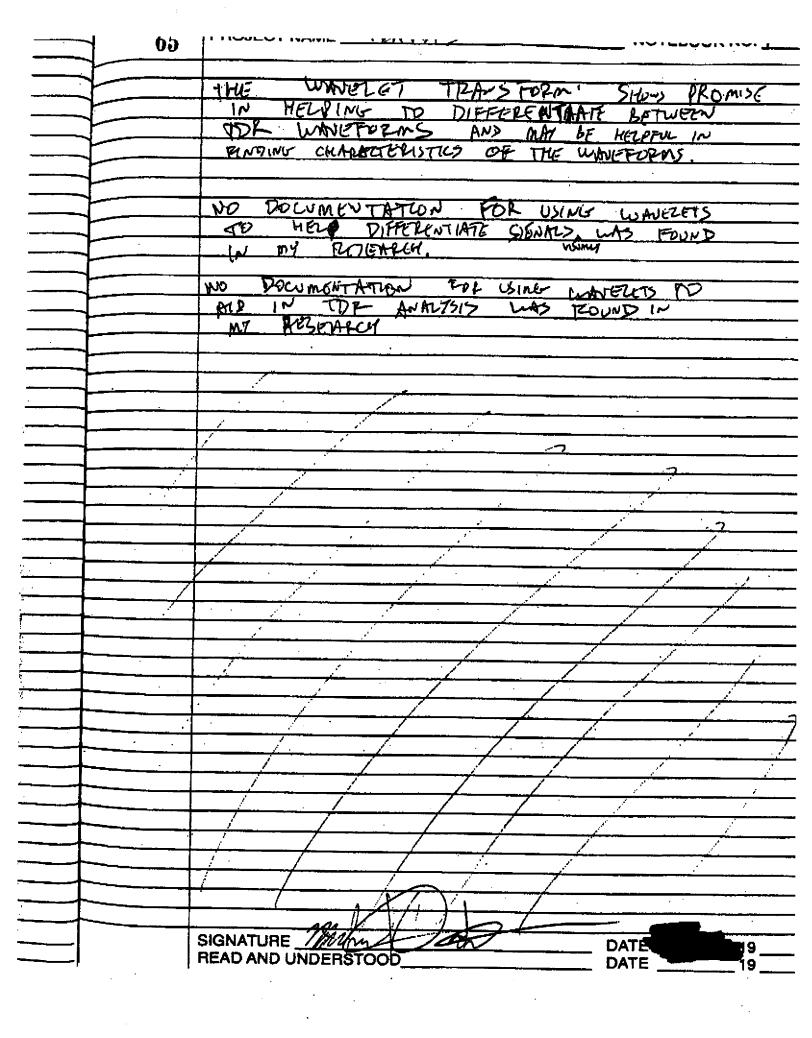






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		PROGRAM IN ATMOSPHEREIC DE OCFANIC SCIENCES http://pass.colorado.edu/research/wavelets
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	SOLUTIONS FOUND:	
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